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PATENT N^o 1

U.S. UTILITY Patent Application

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SCANNED EW (S) Q.A. CR

PATENT DATE

APPLICATION NO. 09/511986	CONT/PRIOR	CLASS 264	SUBCLASS	ART UNIT 1732	EXAMINER <i>[Signature]</i>
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Vernon Williams

TITLE Semiconductor elements, and methods

1.1 **Stereolithographic methods for semiconductor device components and assemblies and conductive elements, and methods**

ANSWER: **100** (100% of 100)

PTO-2040
12/99

ISSUING CLASSIFICATION

Continued on Issue Slip Inside File Jacket

TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
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				Amount Due	Date Paid
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